



Sol–Gel synthesis of SrBi₂Ta₂O₉ nanowires

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ABSTRACT

We report the synthesis of perovskite ferroelectric nanowire strontium bismuth tantalate (SrBi₂Ta₂O₉, SBT). Sol–gel techniques have been used. The morphology and structures are analyzed via SEM, TEM and XRD. The XRD and TEM analysis show that the structure of SBT nanowires is single orthorhombic perovskite with *A2₁am* structure space group. The obtained SBT nanowires with an average length of 35 μm and a diameter of 200 nm are c-axis preferred orientation in the templates. The formation mechanism of SBT nanowires with complex crystal structure was discussed.

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1. Introduction

The synthesis of nano-sized materials has been a very active research field over the past decade years for a variety of reasons [1–3]. The process of miniaturization is important, in both microelectronics industries and microelectromechanical systems (MEMS). Ferroelectric nanostructure has stimulated extensive efforts due to their prospective applications in diverse areas including pyroelectric detectors, piezoelectric inkjet printers and memory capacitors [4,5]. Compared with the synthesis of the general nanotubes such as carbon nanotubes with simple crystal structure, the synthesis of ferroelectric compound is difficult due to the multielement and the complex crystal structures of these ferroelectrics. Fortunately, hard-template synthesis of one-dimensional ferroelectric is a versatile and inexpensive technique for producing nanostructures [6–9]. Size, shape, and structural properties of the assembly could be simply controlled by the template. Nanotubes of different ferroelectric oxides including PbZr_{1-x}Ti_xO₃ (PZT), BaTiO₃, PbTiO₃ and Bi_{4-x}La_xTiO₃ (BLT) have been synthesized by immersing a template membrane in solution [10–12]. Moreover, Morrison et al synthesized SBT nanotubes using a special equipment in photonic Si crystals template [13], and Limmer et al. [14] synthesized a PZT nanowire using electrophoretic sol–gel method in track-etched polycarbonate (PC). Nanowires can be obtained depending on the interfacial adhesion and the solidification modes [15], in other word depending on kind and content of solvent and process of heat treat-

ment. However, to the best of our knowledge, the SBT nanowire has been rarely seen. Herein, we report synthesis of one-dimensional SBT nanowires on anodic alumina (AAO) templates by immersing a template membrane in sol.

2. Experimental

SBT nanowires were prepared with a 0.1 M sol made by dissolving Ta(OEt)₅ and strontium acetate (Sr-(OAc)₂) in ethylene glycol (EG) at 20 °C. Solution of the bismuth subnitrate (BiONO₃) in EG and HAC was prepared, and was added to the Ta–Sr solution. Anodic Al₂O₃ membranes (Whatman Corporation, AAO) with a pore size of 200 nm served as template and were dipped into the sol for 15 min, and then dried in air for 2 h. This process was repeated. The samples were calcined in air at 700 °C for 0.5 h. This process to synthesize SBT sol has been described in our previous report [16,17]. Preprocessing AAO template using HAC solvent and adjust constituents of dipping sol (HAC:EG = 7:2) can effectively reduce surface tension of sol ($\sigma_{EG} = 0.05$ N/m, $\sigma_{HAC} = 0.03$ N/m).

The morphologies and structures of the SBT nanowires were determined by the field-emission scanning electron microscope (SEM, Hitachi S-4700), X-ray diffractometer (XRD, Rigaku D/max-rB), transmission electron microscope (TEM, Philips CM-12). For SEM and TEM observations, the AAO template embedded with SBT nanowires were soaked in a 4 M NaOH solution to remove the alumina completely, and then the resulting product was washed in the deionized water for several times. For TEM investigation, the SBT nanowires were put in the deionized water and subjected to the ultrasonic treatment for several minutes, and then a drop of suspension was placed on the carbon-coated copper grid to carry out TEM studies. To measure the P–E hysteresis loops of the SBT nanowire arrays, both surfaces of the arrays with the template were firstly polished carefully with sand papers until SBT nanowires were emerged, and then a layer of Au with a thickness of 100 nm was sputtered on both sides of the template as electrode. The measurement was performed at a Radiant Technologies Precision Workstation ferroelectric test system.

3. Results and discussion

Scanning electron microscopy (SEM) images of the as-prepared products were shown in Fig. 1. SBT nanowires had a length of 35 μm

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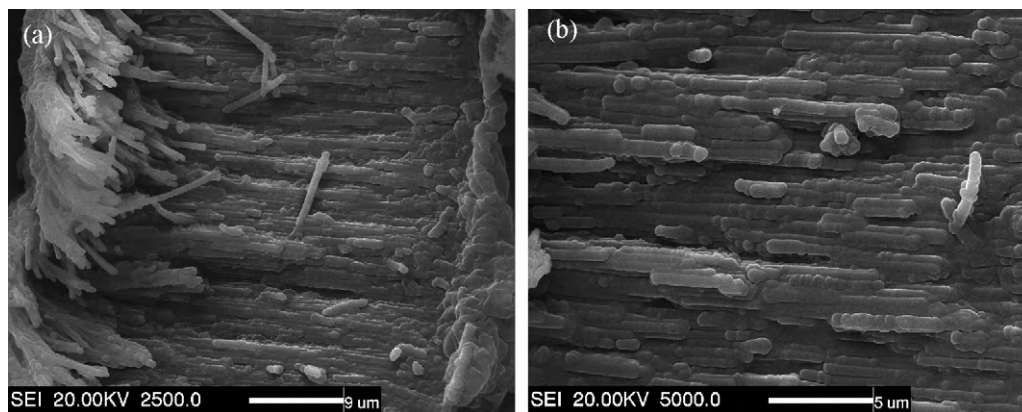


Fig. 1. SEM micrographs of SBT nanowires.

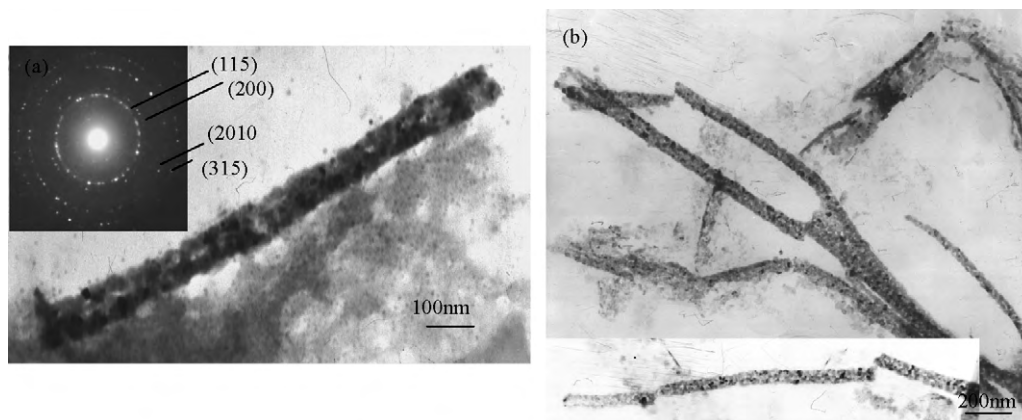


Fig. 2. TEM of the as-synthesized SBT nanowires (a) micrograph and diffraction patterns and (b) nanowires suffered damage.

and a diameter of 200 nm and are considered to be throughout the entire length of the template channel. The sol-gel process was so uniform that a complete covering of the pore occurred.

Typical TEM images of SBT nanowires were presented in Fig. 2, in which the inside solid region was clearly identified. In Fig. 2b, nanowires have suffered damage during handling sample. Electron diffraction patterns were presented in the inset of in Fig. 2a. The different rings in Fig. 2a indicate that the nanowires are composed of small polycrystalline grains. Further analysis on the electron diffraction patterns revealed that nanowire had a perovskite structure, which matched well with the values from XRD results (in Fig. 3).

Fig. 3 shows the XRD pattern of the nanowires. As it indicated, SBT crystallized in the orthorhombic (ferroelectric perovskite, JCPDS 490609) phase at 700 °C. SBT nanowires are almost grown in *c*-axis, as shown in Fig. 3. If the surface and interior of a finite-diameter 1D nano-object is rigidly occupied by atoms, it is often called a nanowire or nanorod. Ordered nanowires fall into several structural classes according to Allen [18]. The basic idea is to (1) choose a maximally linear, charge-neutral, and (if possible) dipole-free atomic cluster containing a single formula unit, and (2) use (if possible) the symmetry axis of this linear cluster as the growth axis and distribute by translations and screw rotations the atomic cluster to give a stoichiometric nanowire with some symmetry around the growth axis and minimal surface polarity. For SBT, the crystal structure still remains orthorhombic phase above T_c (608 K) and changes to tetragonal phase above 900 K [19]. The displacive ferroelectric $\text{SrBi}_2\text{Ta}_2\text{O}_9$ is described at room temperature in space group $A2_1am$ as a commensurate modulation of an idealized $Fm\bar{3}m$ parent structure [20]. Since this $Fm\bar{3}m$ structure may itself be described as a

derivative of a structure of $I4/mmm$ symmetry, it has been customary to define *c*-axis as the long axis and to select *a*-axis as the polar axis direction for ferroelectricity.

The crystal structure of $\text{SrBi}_2\text{Ta}_2\text{O}_9$ consists of Bi_2O_2 layers and perovskite-type SrTa_2O_7 units with double TaO_6 octahedral layers. And it was studied by neutron and electron diffraction, which revealed orthorhombic distortion with space group $A2_1am$. The structural distortion with this noncentrosymmetric space group is responsible for the displacive-type ferroelectric behavior of the compound, that is, atomic displacements along the *a*-axis form cor-

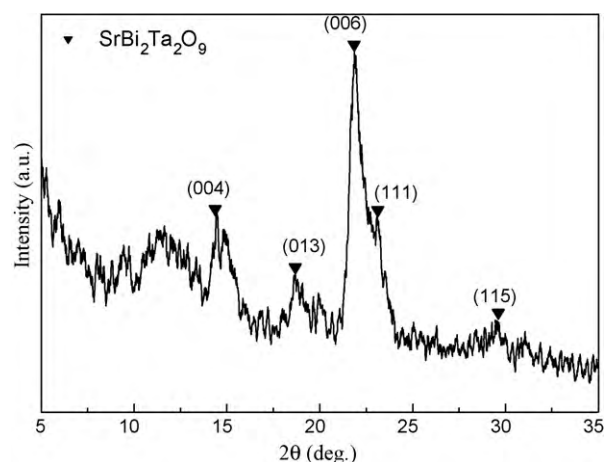


Fig. 3. XRD pattern of the as-synthesized nanowires with AAO membrane.

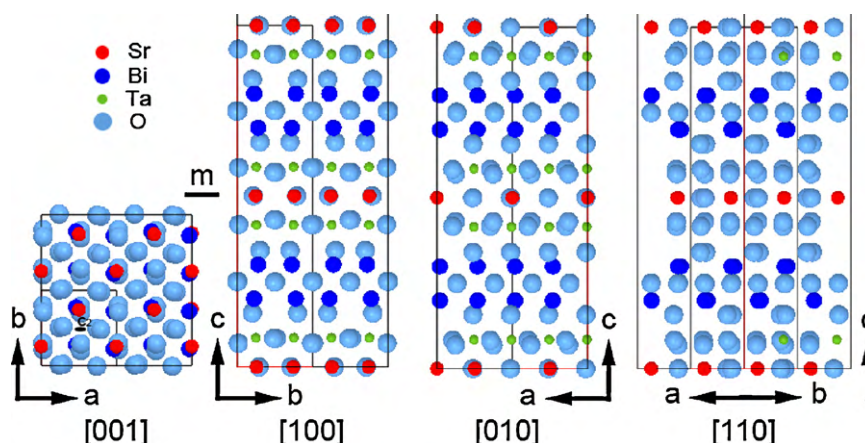


Fig. 4. The projection of atoms position of $\text{SrBi}_2\text{Ta}_2\text{O}_9$ unit cell with space group of $A21am$ in different directions. $[001]$ direction shows 2×2 unit cells, others 2×1 . The original unit cell was framed out. Please note the C_2 screw axis in $[001]$ projection and the mirror planes in other projections.

responding positions in the parent tetragonal ($I4/mmm$) structure cause spontaneous ferroelectric polarization [9]. In Fig. 4 shows the projection of atoms position in different directions. The original unit cell was framed out with $[001]$ direction showing 2×2 unit cells, others 2×1 . There is a mirror plane just at the middle of and perpendicular to c -axis. There is also a C_2 screw axis. Both are also indicated in Fig. 4. A simple way to make a charge-neutral nanowire with nonpolar surfaces is by choosing the c -axis as the growth axis, and thinking of the nanowire as being built from c -axis-oriented Bi_2O_2 and SrTa_2O_7 pairs. Thus c -axis-oriented $\text{SrBi}_2\text{Ta}_2\text{O}_9$ nanowires (Fig. 3) are an obvious choice of structure, and are very favorable for theoretical modeling due to the symmetry. The terminal surfaces are nonpolar layers either of Sr–O, Ta–O, Bi or O with the dangling bonds compensated by adsorbed species like H^+ or OH^- [21]. Thus the whole nanowire has no net dipole with charge-neutral state. As considering other planes, for example (100) , (010) or (110) planes, the difficulty is that these planes are dipolar. So the exposed surface cannot be terminated by these planes. This means that it is impossible for the nanowires to grow along $[100]$, $[010]$, $[110]$ or other directions.

For ferroelectric property measurement, Au top dots of $100 \mu\text{m}$ diameter had been deposited on test SBT nanowires with the Au bottom continuous electrodes. Ferroelectric switching of the sample were typically measured from hysteresis loops as shown in Fig. 5. The low charges may be due to the property of the sam-

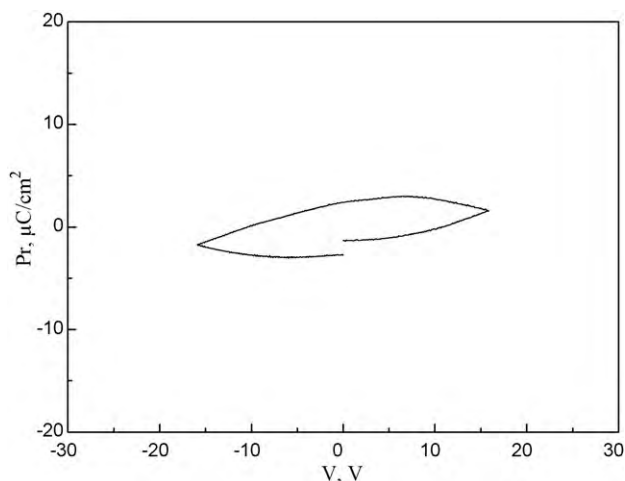


Fig. 5. Hysteresis loop of SBT nanowires with AAO membrane.

ples, which were fabricated from the loose combination of clusters nanowire. Then, it is reasonable to expect that individual nanowire of such material with a much more highly ordered structure will show a higher charge. As displayed by the loops, the charges decreased after reaching the max polarization, which should be attributed to the leakage current caused by the adsorbed moisture and interspaces between nanowires. The hysteresis loop was not saturated, which could be solved by making smaller electrode consisting fewer nanowires.

4. Conclusions

Finely arrayed $\text{SrBi}_2\text{Ta}_2\text{O}_9$ nanowires can be prepared by immersing a template membrane in sol with reducing surface tension. To minimize surface polarity, SBT nanowires oriented preferentially along the growing axis (c -axis) by translation and rotation of atomic clusters of SBT. There is a need to deposit ferroelectric nanowire on the inside of trenches for Tera bits random-access memories (RAMs), both dynamic DRAMs and non-volatile ferroelectric FRAMs.

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